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APPLICANTS

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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN 2002-284211 09/27/2002

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 2
Verified and Acknowledged Examiner's Signature	Initials				

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TITLE

Semiconductor device including interconnects formed by damascene process and manufacturing method thereof

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